

Abstracts

60 GHz high-efficiency HEMT MMIC chip set development for high-power solid state power amplifier

Y. Hwang, J. Lester, G. Schreyer, G. Zell, S. Schrier, D. Yamauchi, G. Onak, B. Kasody, R. Kono, Y.C. Chen and R. Lai. "60 GHz high-efficiency HEMT MMIC chip set development for high-power solid state power amplifier." 1997 MTT-S International Microwave Symposium Digest 3. (1997 Vol. III [MWSYM]): 1179-1182.

A 60 GHz high-efficiency HEMT MMIC chip set was developed including two fully-matched HEMT MMIC chips. A 2-mil thick MMIC with 300 mW output power and 22% efficiency and a 4-mil thick gain stage MMIC are described. They were used as building blocks of high power (1 to 50 watts) SSPA.

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